IEEE Computer Society Annual Symposium on VLSI
Miami, Florida, U.S.A., July 15-17, 2019

Home

Travel/Accommodation

Keynotes / Panel

Miami Attractions

Social Events

Calls

Committees

Important Dates

Paper Submission

Student Research Forur

Program Schedule

Registration

NSF Student Travel Grant

Sister Conferences

Past Conferences

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NEWS & UPDATES

- **NEWS:** The **slides** of ISVLSI 2019 keynote are available at Keynotes page!
- **NEWS:** **ISVLSI photos** are available via following link:

  https://www.flickr.com/photos/182810011@N07/.

- **NEWS:** **ISVLSI online proceedings** are available via following link:

  https://conferences.computer.org/isvlsi/2019/#!/home.

  We will send you the **login ID and password** through your **registered email address**.

- **NEWS:** ISVLSI2019 provides **complementary parking** for attendees, please register your car at your **requested date** here.
- **NEWS:** **WIFI Network**: The user with eduroam account will connect automatically. There is also WiFi network **"FIU_WiFi"** for free access, no password is needed.
- **NEWS:** The **Dinner Cruise** **pick up bus** is scheduled on **July 16th, 2019 at 4:45 PM**, in front of the **Academic I building**.
- **NEWS:** ISVLSI2019 whova online program is now ready to download:

  Get Whova Now   Invitation code: **isvlsi2019**

- **NEWS:** ISVLSI2019 **Conference Program** is now ready: PDF version
- **NEWS:** **NSF student travel grants application** is now available: Application link
- **NEWS:** ISVLSI2019 exciting **social event** -- **Dinner Cruise that you CANNOT miss in Miami!**
- **NEWS:** **Call for Proposals in Research Demo Session (RDS)** is now avaible: Submission link
- **NEWS:** **ISVLSI2019 camera ready paper submission** at: link
- **NEWS:** **Conference registration** is now OPEN: https://www.regonline.com/isvlsi2019
- **NEWS:** Update the **Visa Supporting Letter** information in Travel/Accommdation page.
- **NEWS:** Update the **Miami Attractions** information.

The IEEE Symposium on VLSI (ISVLSI) 2019 explores emerging trends, novel ideas and basic concepts covering related topics: from VLSI systems, tools and design methods at different abstraction levels, to bringing VLSI d new technologies such as nano and molecular devices and burgeoning application areas, such as hardwar intelligence. Future design methodologies are also one of the key topics at the symposium, as well as new EDA Over three decades ISVLSI has been a unique forum promoting multidisciplinary research and new visionary ap VLSI, bringing together leading scientists and researchers from academia and industry. The ISVLSI proceedings w Computer Society Press. Selected papers from past editions have been subsequently published in special issues ISVLSI has a good reputation of bringing together well-known international scientists as invited speaks. ISVLS momentum and carry forward these well-established trends for further growth of the symposium.

Contributions are sought in, but not limited to, the Following Tracks

1) Circuits, Reliability, and Fault-Tolerance (CRT): Analog/mixed-signal circuits design and testing, RF and comm for testability and reliability, adaptive circuits, interconnects, static and dynamic defect-and fault-recoverabili design.

2) Computer-Aided Design and Verification (CAD): Hardware/software co-design, logic and behavioral synthesi verification, physical design, signal integrity, power and thermal analysis , statistical approaches.

3) Digital Circuits and FPGA based Designs (DCF): Digital circuits, chaos/neural/fuzzy-logic circuits, high-sp energy efficient circuits, near and sub-threshold circuits, memories, FPGA designs, FPGA based systems.

4) Emerging and Post-CMOS Technologies (EPT): Nanotechnology, molecular electronics, quantum devices, based computing, biologically-inspired computing, CNT, SET, RTD, QCA, reversible logic, and CAD tools for emer and circuits.

5) System Design and Security (SDS): Structured and Custom Design methodologies, microprocessors performance and low power, embedded processors, analog/digital/mixed-signal systems, NoC, power and tem Hardware security, Cryptography, watermarking, and IP protection, TRNG and security oriented circuits, PUF circu

6) VLSI for Applied and Future Computing (AFC): Neuromorphic and brain-inspired computing, quantum architectures for machine learning and artificial intelligence, methodologies for on-chip learning, deep learning applications for and use-cases of learning systems, sensor and sensor network, electronics for Internet of Th devices.

The Symposium Program will include contributed papers and speakers invited by the Program Committee as w
The keynotes, special sessions and Graduate Student Forum are planned as well. Authors are invited to sub
unpublished papers. To enable blind review, the author list should be omitted from the main document.

For Questions and Requests, please contact the Conference Organizers.

Paper Submission: Authors are invited to submit full-length (6 pages maximum), original, unpublished papers
of at most 200 words. To enable blind review, the author list should be omitted from the main document. Pap
blind-review criteria would be withdrawn from the review process. Previously published papers or papers cu
other conferences/journals should not be submitted and will not be considered for publication.

Paper Submission Site: https://easychair.org/conferences/?conf=isvlsi2019

Important Dates:

- ♦ Paper Submission Deadline: ~~February 17, 2019~~   **EXTENDED:** March 3, 2019

- ♦ Acceptance Notification: April 21, 2019

- ♦ Submission of Final Version: May 12, 2019

This site is maintained by:
ISVLSI 2019 Web Chair